

Tutorial Quiz Questions Week 10

1. What do the acronyms TTL and CMOS stand for?
2. If a TTL inverter input is a logic low, does current flow into or out of the input?
3. True or False. The typical output voltage for a logic high on the output of a TTL device is 5V. (False)
4. What term is used to describe the maximum number of identical gates which can be driven by a single gate? (fan out)
5. Two of the most commonly used logic families are usually referred to as the 74LS family and the 74HC family. What do the letters LS and HC stand for?